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Docket No.: M-11590 US

January 2, 2002

T: 415-217-6000

F: 415-434-0646

Box Patent Application Commissioner For Patents Washington, D. C. 20231

Three Embarcadero Ctr.

Enclosed herewith for filing is a patent application, as follows:

San Francisco

California 94111

Inventor(s):

Nam, Shibaek; Jeon, Oseob; Heo, Chulho

Title:

Semiconductor Device Having Multi-Chip Package Structure

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This Transmittal Letter (in duplicate)

page(s) Specification (not including claims)

page(s) Claims page Abstract

Sheet(s) of Drawings (For Figs. 1, 2, 3, 4, 5 and 6)

page(s) Declaration For Patent Application and Power of Attorney

page(s) Recordation Form Cover Sheet (in duplicate) page(s) Assignment

**CLAIMS AS FILED** 

<u>For</u> Total Claims	Number <u>Filed</u> 60	-20	=	Number <u>Extra</u> 40	x	<u>Rate</u> \$18.00	=	<b>\$</b> \$	Basic Fee <u>740.00</u> 720.00
Independent Claims	8	-3	==	5	х	\$84.00	=	\$	420.00
Fee of for the first filing of one or more multiple dependent claims per application								\$	
Fee for Request for Extension of Time									
Pleas	e make the fol	lowing	charg	es to Deposit	Accon	nt 19-2386			

Total fee for filing the patent application in the amount of

\$ 1,880.00

The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account 19-2386.

EXPRESS MAIL LABEL NO:

EL 873075491 US

Respectfully submitted,

Philip W. Woo

Attorney for Applicant(s)

Reg. No. 39,880